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FACSIMILE COVER SHEET

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)
POIROUX ET AL.)
Serial No. 10/672,931) Attorney Docket No. 54480
Filing Date: SEPTEMBER 26, 2003)
For: PROCESS AND DEVICE FOR)
EVALUATING A CMOS LOGICAL CELL)
_____)

REQUEST FOR CORRECTION TO THE FILING RECEIPT


Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The filing receipt for the subject patent application has been received and reviewed for accuracy. Three discrepancies have been discovered. The filing receipt in error lists the first name of the second listed inventor as "Philippe". The correct spelling is Philippe. The next error lists the name of the first listed Assignee as "STMicroelectronics S.A.". The correct name is STMicroelectronics SA.

In view of the errors noted, it is respectfully requested that a corrected filing receipt be issued in this application. If any fee is believed to be necessary, authorization is given to charge Deposit Account No. 01-0484.

Respectfully submitted,



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In re Patent Application of
POIROUX ET AL.
Serial No. 10/672,931
Filed: SEPTEMBER 26, 2003

CERTIFICATE OF FACSIMILE

I HEREBY CERTIFY that this REQUEST FOR CORRECTION TO
THE FILING RECEIPT for U.S. Serial No. 10/672,931 is being
submitted via telefacsimile no. 703-746-9195 to: OFFICE OF
INITIAL PATENT EXAMINATION'S FILING RECEIPT CORRECTIONS, P.O.
BOX 1450 ALEXANDRIA, VA 22313-1450, this 18th day of March,
2004.

Dawn K. Limber



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REGD	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/672,931	09/26/2003	2819	1062	02GR118254480	8	29	3

CONFIRMATION NO. 9497

27975

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UPDATED FILING RECEIPT

OC000000011941975

Date Mailed: 02/20/2004

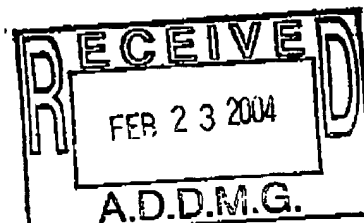
Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Thierry Poiroux, Voreppe, FRANCE;
 Philippe Flatresse, Froges, FRANCE;
 Philippe

Assignment For Published Patent Application

STMicroelectronics S.A., Montrouge, FRANCE;
 Commissariat A L'Energie Atomique, Paris Cedex, FRANCE;



Domestic Priority data as claimed by applicant

Foreign Applications

FRANCE 0212022 09/27/2002

If Required, Foreign Filing License Granted: 01/02/2004

Projected Publication Date: 06/03/2004

Non-Publication Request: No

Early Publication Request: No

Title

Process and device for evaluating a CMOS logical cell

Preliminary Class

326

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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